

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1-34. (Cancelled)

35. (Previously Presented) A driver circuit, comprising:

a first storage capacitor;

a second storage capacitor;

an n-channel transistor, of which a gate is connected to the first storage capacitor; and

a p-channel transistor, of which a gate is connected to the second storage capacitor,

a current driven element being disposed between the n-channel transistor and the p-channel transistor,

a data current according to a data signal flowing through the p-channel transistor and the n-channel transistor so that a first operating voltage of the n-channel transistor and a second operating voltage of the p-channel transistor are set by the first storage capacitor and the second storage capacitor, and

the n-channel transistor and the p-channel transistor operatively controlling, in combination, a driving current according to the data signal supplied to a current driven element.

36. (Previously Presented) The driver circuit as claimed in claim 35,

further comprising first switching means,

the first switching means and a source of the data current being connected so as to provide when operative a current source for the current driven element.

37. (Previously Presented) The driver circuit as claimed in claim 35,

further comprising first switching means,  
the first switching means and a source of the data current being connected so  
as to provide when operative a current sink for the current driven element.

38. (Previously Presented) The driver circuit as claimed in claim 35,  
further comprising a second switching means,  
the second switching means being connected to bias the n-channel transistor  
and the p-channel transistor to act as diodes respectively when the data current flows through  
the n-channel transistor and p-channel transistor.

39. (Previously Presented) The driver circuit as claimed in claim 35,  
the n-channel transistor and the p-channel transistor being polysilicon thin film  
transistors.

40. (Previously Presented) The driver circuit as claimed in claim 35,  
the current driven element being an electroluminescent element.

41. (Previously Presented) The driver circuit as claimed in claim 35,  
the n-channel transistor and the p-channel transistor being arranged in close  
proximity to each other.

42. (Previously Presented) A driving method of a driver circuit that is for a  
current driven element and that has an n-channel transistor, a p-channel transistor, the current  
driven element being disposed between the n-channel transistor and the p-channel transistor,  
a first storage capacitor connected to a gate of the n-channel transistor, and a second storage  
capacitor connected to a gate of the p-channel transistor, comprising:

a first step for setting a first operating voltage of the n-channel transistor and a  
second operating voltage of the p-channel transistor by supplying a data current according to  
a data signal to the n-channel transistor and the p-channel transistor; and

a second step for supplying a current that is controlled by the n-channel transistor and the p-channel transistor in combination to the current driven element.

43. (Previously Presented) The driving method as claimed in claim 42, in the first step, the n-channel transistor and the p-channel transistor acting as a diode.

44. (Previously Presented) The driving method as claimed in claim 42, the current driven element being an electroluminescent element.

45. (Previously Presented) An electro-optical device comprising the driver circuit according to claim 35.

46. (Previously Presented) An electronic apparatus incorporating an electro-optical device according to claim 45.

47. (Currently Amended) A driver circuit for a current driven element, comprising:

a storage capacitor;

a driving transistor of which a gate is connected to the storage capacitor;

an n-channel transistor; and

a p-channel transistor,

an operating voltage of the driving transistor being set by the storage capacitor by flowing a data current according to a data signal,

a data driving current according to a data signal that flows through the current driven element flowing through the n-channel transistor, the p-channel transistor and the driving transistor so that an operating voltage of the driving transistor is set by the storage capacitor, and

the current driven element being disposed between the n-channel transistor and the p-channel transistor.

48. (Previously Presented) The driver circuit according to claim 47,  
the n-channel transistor and the p-channel transistor being controlled by an  
identical signal.

49. (Previously Presented) A driver circuit, comprising:  
a first storage capacitor;  
a second storage capacitor;  
an n-channel transistor of which a gate is connected to the first storage  
capacitor;  
a p-channel transistor of which a gate is connected to the second storage  
capacitor;  
a current driven element disposed between the n-channel transistor and the p-  
channel transistor;  
a first switching transistor connected between a drain of the n-channel  
transistor and the first storage capacitor; and  
a second transistor connected between a drain of the p-channel transistor and  
the second storage capacitor.

50. (Previously Presented) A driver circuit, comprising:  
a first storage capacitor;  
a second storage capacitor;  
a first n-channel transistor of which a gate is connected to the first storage  
capacitor;  
a first p-channel transistor of which a gate is connected to the second storage  
capacitor;  
a second n-channel transistor;  
a second p-channel transistor;

a current driven element disposed between the second n-channel transistor and the second p-channel transistor;

a first switching transistor connected between a drain of the first n-channel transistor and the first storage capacitor; and

a second switching transistor connected between a drain of the first p-channel transistor and the second storage capacitor.

51. (Previously Presented) The driver circuit according to claim 50,  
the second n-channel transistor and the second p-channel transistor being controlled by an identical signal.
52. (Previously Presented) The driver circuit according to claim 50,  
the first n-channel transistor being connected to the first p-channel transistor.
53. (Previously Presented) The driver circuit as claimed in claim 50,  
the current driven element being an organic electroluminescent element.
54. (Previously Presented) An electro-optical device comprising the driver circuit according to claim 50.
55. (Previously Presented) An electronic apparatus incorporating an electro-optical device according to claim 54.
56. (Previously Presented) The driver circuit as claimed in claim 49,  
the current driven element being an organic electroluminescent element.
57. (Previously Presented) An electro-optical device comprising the driver circuit according to claim 49.
58. (Previously Presented) An electronic apparatus incorporating an electro-optical device according to claim 57.